

Appl. No. 09/976,504

REMARKS/ARGUMENTS

The foregoing amendments to the specification are presented to clarify the official record in respect of the above-identified application.

The amended paragraph of the specification properly indicates that this application claims "the benefit of", instead of "priority from", the earlier patent applications that were already identified in the specification.

For the assistance of the Examiner, Applicant has also clarified that the present application is a divisional of parent U.S. Patent Application Serial No. 09/851,565, and that the parent application is a continuation-in-part of U.S. Patent Applications Serial Nos. 09/823,667 and 09/779,381.

Applicant notes that this application had previously been amended to include specific references to the earlier applications of which the benefit is claimed under 35 U.S.C. 120. The above amendments merely clarify the benefit claims.

In the Office Action, claim 25 was rejected under 35 U.S.C. 102(c) as being anticipated by United States Patent 6,535,513 (hereinafter "Kao"). The paragraph bridging pages 2-4 of the Office Action refers primarily to Figure 4 of Kao and corresponding portions of columns 5 and 6 as allegedly disclosing the system recited in claim 25.

According to claim 25, a system includes a first integrated circuit having a first bus interface, a second bus interface, and a control interface, with a data path extending from the first bus interface, through segmentation circuitry on the first integrated circuit, through reassembly circuitry on the first integrated circuit, and to the second bus interface. It is alleged in the Office Action that the claimed first integrated circuit is disclosed in Kao in the form of the Cubit chip 424A (Figure 4) which connects to an ingress UTOPIA element 402, a shared bus 306A, and a FIFO control 414. The Office Action further alleges that a data path 410 extends from the

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ingress UTOPIA element 402 through segmentation circuitry in the ATMizer chip 422, through reassembly circuitry on the ATMizer chip 422, and to the shared bus 306A.

It is first noted that the Office Action refers to multiple chips and elements in Figure 4 of Kao as supposedly forming an integrated circuit. Clearly, the ingress UTOPIA element 402, the shared bus 306A, the FIFO control 414, the data path 410, and the ATMizer 422 are not part of the Cubit chip 424A, which in the Office Action is alleged to be a first integrated circuit.

Furthermore, Kao does not disclose a data path as recited in the claim. Claim 25 clearly defines a data path extending from the first bus interface on the first integrated circuit, through segmentation circuitry on the first integrated circuit, through reassembly circuitry on the first integrated circuit, and to the second bus interface on the first integrated circuit. Beyond the fact that Kao does not disclose the claimed interfaces and data path on one integrated circuit, Kao also does not disclose the particular claimed sequence of elements along the data path. Although the ATMizer chip 422 may be used for segmentation and reassembly, these functions in such a chip would not be connected to each other in a single data path such that the data path extends through the segmentation function and also through the reassembly function. Therefore, a data path extending from the first bus interface, through segmentation circuitry on the first integrated circuit, through reassembly circuitry on the first integrated circuit, and to the second bus interface, as recited in claim 25, has not been disclosed in Kao.

Claim 25 also recites a second integrated circuit that is substantially structurally identical to the first integrated circuit. Accordingly, the claimed second integrated circuit has a first bus interface, a second bus interface, and a control interface, and also a data path extending from the first bus interface, through segmentation circuitry on the second integrated circuit, through reassembly circuitry on the second integrated circuit, and to the second bus interface. As discussed in detail above in respect of the first integrated circuit, Kao also does not disclose a second integrated circuit as recited in independent claim 25.

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The flow control functions of the control integrated circuit recited in claim 25 further distinguish the claim over Kao at least to the extent that the controlled flows pass over the data paths of the first and second integrated circuits, which have not been disclosed in Kao.

It is therefore respectfully submitted that Kao does not disclose the limitations of independent claim 25, and that the anticipation rejection under 35 U.S.C. 102(c) should be reconsidered and withdrawn.

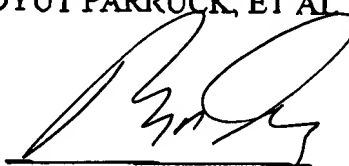
Applicant thanks the Examiner for the indication on page 4 of the Office Action that claims 26-28 and 45-52 include allowable subject matter, and that claims 53-60 are allowed. As discussed in detail above, independent claim 25 is also believed to be allowable over Kao.

In view of the foregoing, Applicant submits that the application is now in condition for allowance, and early action to this end is earnestly solicited.

Respectfully submitted,

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